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09/29/2005

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DALLAS, TX 75265

Attn: Jacqueline J. Garner

12/12/2005 TBESHAH2 00000002 200668 10649425

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Marianna Smith (Depositor's name)
Marianna Smith (Signature)
12-9-05 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/649,425	08/27/2003	James J. Chambers	TI35714	5169

TITLE OF INVENTION: STRUCTURE AND METHOD TO FABRICATE SELF-ALIGNED TRANSISTORS WITH DUAL WORK FUNCTION METAL GATE ELECTRODES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	12/29/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
SARKAR, ASOK K	2891	438-585000

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- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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1 Jacqueline J. Garner
2 W. James Brady III
3 Frederick J. Telecky, Jr.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

Texas Instruments Incorporated

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Dallas, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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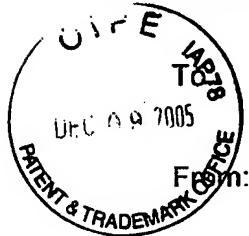
Typed or printed name Jacqueline J. Garner

Date 12/08/05

Registration No. 36,144

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: James J. Chambers
Serial No: 10/649,425
Filed: 08/27/2003
Art Unit: 2891
Examiner: Asok K. Sarkar
Docket No.: TI-35714
Conf. No.: 5169
Customer No.: 23494

CERTIFICATION OF FACSIMILE TRANSMISSION

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NAME OF INVENTOR(S): James J. Chambers		RECEIPT DATE & SERIAL NO.: Serial No.: 10/649,425	
TITLE OF INVENTION: FERAM Capacitor Stack Etch		Filing Date: 8/27/2003	
TI FILE NO.: TI-35714		Conf. No.: 5169	
DEPOSIT ACCT. NO.: 20-0668			
FAXED: 12/9/2005			
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